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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/816,932	03/23/2001	Francisco Jose Francisco Pires	GR 00 P 1568	6127

7590

12/06/2001

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EXAMINER

VU, QUYNH NHU H

ART UNIT	PAPER NUMBER
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2841

DATE MAILED: 12/06/2001

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/816,932

Applicant(s)

FRANCISCO PIRES, FRANCISCO JOSE

Examiner

Quynh-Nhu H. Vu

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-4 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-4 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on ____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) ____.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

DETAILED ACTION

Drawings

1. Figure 5 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g).

Specification

2. The following guidelines illustrate the preferred layout and content for patent applications. These guidelines are suggested for the applicant's use.

Arrangement of the Specification

The following order or arrangement is preferred in framing the specification and, except for the reference to "Microfiche Appendix" and the drawings, each of the lettered items should appear in upper case, without underlining or bold type, as section headings. If no text follows the section heading, the phrase "Not Applicable" should follow the section heading:

- (a) Title of the Invention.
- (b) Cross-References to Related Applications.
- (c) Statement Regarding Federally Sponsored Research or Development.
- (d) Reference to a "Microfiche Appendix" (see 37 CFR 1.96).
- (e) Background of the Invention.
 1. Field of the Invention.
 2. Description of the Related Art including information disclosed under 37 CFR 1.97 and 1.98.
- (f) Brief Summary of the Invention.
- (g) Brief Description of the Several Views of the Drawing(s).
- (h) Detailed Description of the Invention.
- (i) Claim or Claims (commencing on a separate sheet).
- (j) Abstract of the Disclosure (commencing on a separate sheet).
- (k) Drawings.
- (l) Sequence Listing (see 37 CFR 1.821-1.825).

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

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(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-4 are rejected under 35 U.S.C. 102(b) as being anticipated by Edwards [US 4,580,193].

Edwards discloses in Fig. 1 a conductor track layer structure precursor stage (6) having a plurality of conductor track (15) disposed on an electrically insulating substrate; said electrically insulating substrate having an inner region and a lateral peripheral region; the conductor tracks connected via conductor track current lines to at least one current line (see attachment for details); wherein the current line extends in the lateral peripheral region of the conductor track layer structure precursor stage; the conductor tracks are each connected to the at least current line via a separate conductor track current line, and the conductor tracks inside the inner region of the conductor track layer structure precursor stage are electrically insulated from one another.

It is noted that the product-by-process limitation of claim 4 has not been give weight in determining the patentability of the device claim. See MPEP §2113.

5. Claims 1-4 are rejected under 35 U.S.C. 102(b) as being anticipated by Janai et al. [US 5,541,814].

Janai et al. disclose in Fig. 2 (also see attachment for details) a conductor track layer structure precursor stage having a plurality of conductor track disposed on an electrically insulating substrate; said electrically insulating substrate having an inner region and a lateral peripheral region; the conductor tracks connected via conductor track current lines (36) to at least one current line (38); wherein the current line extends in the lateral peripheral region of the conductor track layer structure precursor stage; the conductor tracks are each connected to the at least current line via a separate conductor track current line, and the conductor tracks inside

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the inner region of the conductor track layer structure precursor stage are electrically insulated from one another.

It is noted that the product-by-process limitation of claim 4 has not been give weight in determining the patentability of the device claim. See MPEP §2113.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Baker [US 3,061,911], Mermet-Guyennet [US 5,243,496] and Kurashima [US 5,365,406] disclose conductor tracks connected with conductor track current lines.

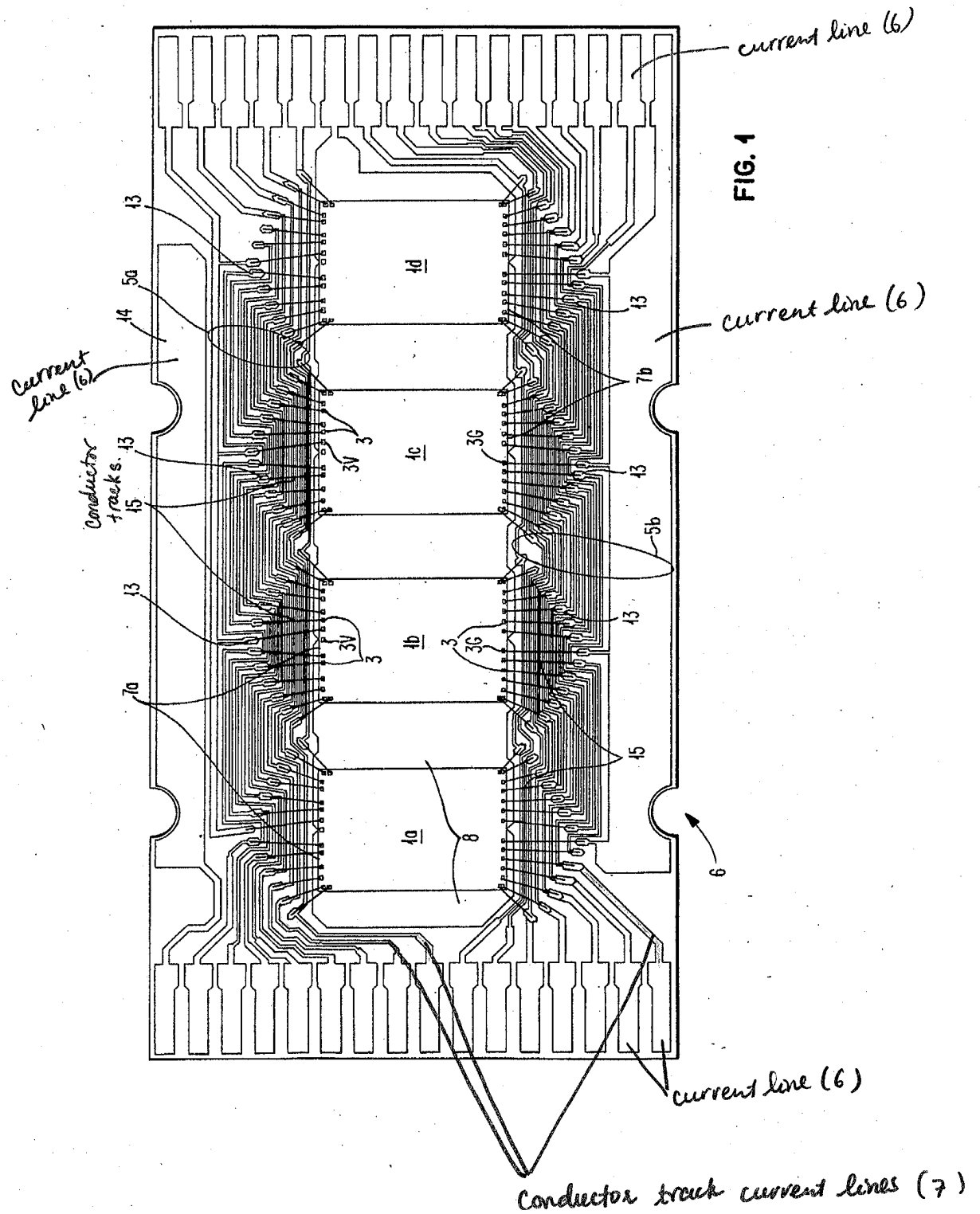
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quynh-Nhu H. Vu whose telephone number is 703-305-0850. The examiner can normally be reached on 7:30-5:00 (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey Gaffin can be reached on 703-308-3301. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7724 for regular communications and 703-308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

QNV
November 28, 2001

Albert W. Paladini 1170-01
ALBERT W. PALADINI
PRIMARY EXAMINER



- conductor tracks (2)
- conductor tracks lines (7)
- current line (6)

